

# SPECIFICATION FOR LCD MODULE MODULE NO: YB-TG19201080C02A-C-B1

 Doc.Version:03

 Customer Approval:

 Accept

 Reject

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# **<u>1. Revision History</u>**

Sample Version	DOC. Version	DATE		DESCRIPTION	CHANGED BY
A0	00	2018-12-10	SPEC ONLY	First issue	Yufang
A1	01	2018-12-18	SPEC ONLY	Change the Specification and connector type	Yufang
A1	02	2019-03-21	FULL SPEC	First Sample	Yufang
A1	03	2019-04-08	FULL SPEC	Add JET-INK CODE	Yufang



# **<u>2. Table of Contents:</u>**

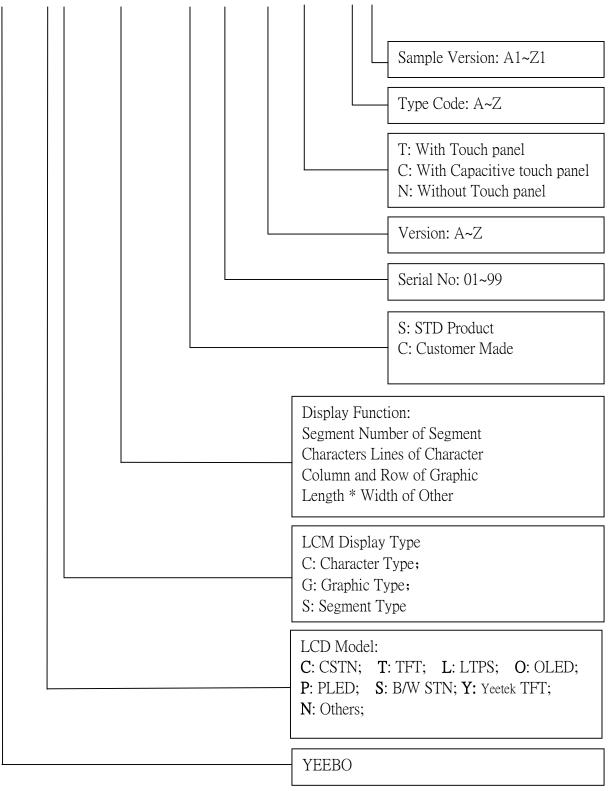
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## 3. Module Numbering System:

(Example)

# <u>YB- TG 19201080 C 02 A -C – B0</u>



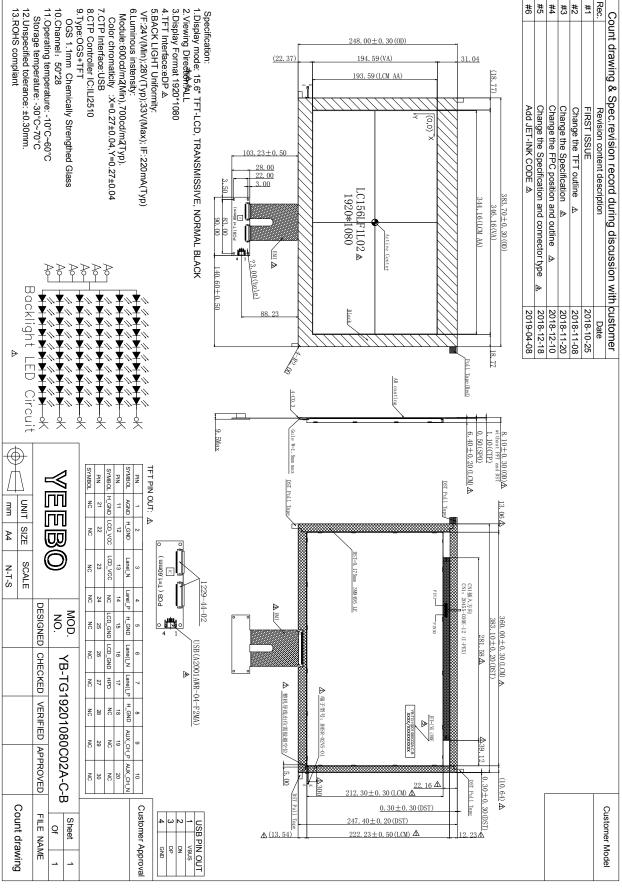


# 4. General Specification:

ITEM	CONTENTS
Assembly Module Size	383.70(W) * 248.00(H) * 8.10(T) mm
Display Size(Diagonal)	15.6 inch
Display Format	1920(RGB)*1080 Pixels
Active Area	344.16(W) * 193.59(H) mm
Pixel Pitch	0.17925 * 0.1335 mm
LCD Type	Normally black, Transmissive
TFT Driver element	a-Si TFT active matrix
TFT Surface treatment	A nti-Glare
Color arrangement	RGB-stripe
TFT interface	EDP
LCM power consumption	9.1W
View Direction	ALL
CTP IC	ILI2510
CTP Interface	USB
Weight(g)	873.6g
Firmware	8984_20190125.hex
Test Configuration	8984_20190123.dat



# **5. CTP+LCM drawing:**



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# 6.interface Pin Assignment

6-1. TFT FPC Connector is used for the module electronics interface. The recommended model is 20455-030E-76 (I-PEX) manufactured by Hirose.

Pin No.	Symbol	I/O	Function	Remark
1	NC	-	No connect	
2	H_GND	Р	High Speed Ground	
3	Lane1_N	Ι	Complement Signal Link Lane 1	
4	Lane1_P	Ι	True Signal Link Lane 1	
5	H_GND	Р	High Speed Ground	
6	Lane0_N	Ι	Complement Signal Link Lane 0	
7	Lane0_P	Ι	True Signal Link Lane 0	
8	H_GND	Р	High Speed Ground	
9	AUX_CH_P	Ι	True Signal Auxiliary Channel	
10	AUX_CH_N	Ι	Complement Signal Auxiliary Channel	
11	H_GND	Р	High Speed Ground	
12-13	LCD_VCC	Р	LCD logic and driver power(3.3V)	
14	NC	-	No connect	
15-16	LCD_GND	Р	LCD logic and driver ground	
17	HPD	Ι	HPD Signal	
18-30	NC	-	No connect	

I: input; O: output; P: Power or Ground(0V).

Note:



# 6-2. CTP Pin Assignment

Pin No.	Symbol	Function
1	VBUS	USB Supply Power
2	DN	USB Data Negative
3	DP	USB Data Positive
4	GND	Ground



## **7.Electrical Characteristics**

#### 7.1Absolute Maximum Ratings

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are for stress ratings. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may remain possibility to affect device reliability.

Table 7-1: Absoulte Maximum Ratings

Parameter	Symbol	Min	Max	Unit
Chip power input	V <sub>DD</sub>	-0.3	3.6	V
V <sub>DD3A</sub> to GND	V <sub>DD3A</sub>	-0.3	3.6	V
V <sub>DD3D</sub> to GND	V <sub>DD3D</sub>	-0.3	3.6	V
V <sub>DDIO</sub> to GND	VDDIO	-0.3	3.6	V
V <sub>DD16</sub> to GND	V <sub>DD16</sub>	-0.3	1.65	V
V <sub>GH</sub> to GND	V <sub>GH</sub>	-0.3	32	V
V <sub>TX</sub> to GND	V <sub>TX</sub>	-0.3	32	V
ESD Susceptibility HBM (Human Body Mode)(Note 1)	HBM		4000	V
ESD Susceptibility MM (Machine Mode)	MM		400	V

Note 1: Devices are ESD sensitive. Handling precaution is recommended.

## 7.2 Recommended Operating Conditions

Parameter	Symbol	Min	Max	Unit
$V_{DD}$ to GND input power supply voltage	V <sub>DD</sub>	3.14	3.46	V
V <sub>DD3A</sub> to GND	V <sub>DD3A</sub>	3.14	3.46	V
V <sub>DD3D</sub> to GND	V <sub>DD3D</sub>	3.14	3.46	V
V <sub>DDIO</sub> to GND	V <sub>DDIO</sub>	1.8	3.46	V
V <sub>GH</sub> to GND	V <sub>GH</sub>	-0.3	32	V
V <sub>TX</sub> to GND	V <sub>TX</sub>	-0.3	32	V
Operating Ambient Temperature Range	ТА	-20	85	°C
Operating Junction Temperature Range	TJ	-40	125	°C
Storage Ambient Temperature Range	T <sub>ST</sub>	-40	150	°C

Note: The device is not guaranteed to function outside its operating conditions.

## 7.3 DC Characteristics

Table 7-3:Input Power Supply

#### (VDD3A = VDD3D = 3.3V, Room Temperature)

Item	Symbol	Min	Тур.	Max	Unit	Condition
USB 3.3V input power supply voltage	V <sub>DD</sub>	3.14	3.3	3.46	V	@ USB
Digital input power supply voltage*	V <sub>DD3D</sub>	3.14	3.3	3.46	V	
Analog input power supply voltage	V <sub>DD3A</sub>	3.14	3.3	3.46	V	
I/O input power supply voltage*	V <sub>DDIO</sub>	1.8	3.3	3.46	V	d

\*If VDDIO & VDD3D is not supplied power, there is risk of I/O pin with current leakage

#### Table 7-4:DC Characteristics

Item	Symbol	Min	Тур.	Max	Unit	Condition
Operation current	lop		100		mA	Active Mode / 21.5"
Input Low Voltage	V <sub>IL1</sub>	0		0.3V <sub>DDIO</sub>	V	
Input High Voltage	V <sub>IH1</sub>	0.6V <sub>DDIO</sub>		V <sub>DDIO</sub> +0.5	v	
Hysteresis voltage	V <sub>HY</sub>		0.2VDDIO		V	
Input Low Voltage, XT_In	V <sub>IL2</sub>	0		0.6	v	V <sub>DDIO</sub> =3.3V
Input High Voltage, XT_In	V <sub>IH2</sub>	2.6		V <sub>DDIO</sub> +0.2	V	V <sub>DDIO</sub> =3.3V
Negative going threshold, /Reset	VILS	0		0.2VDDIO	V	
Positive going threshold, /Reset	VIHS	0.6V <sub>DDIO</sub>		V <sub>DDIO</sub> +0.5	v	
Output High Voltage	V <sub>OH</sub>	0.7V <sub>DDIO</sub>			v	V <sub>DDIO</sub> =3.3V, I <sub>OH</sub> =8mA
Output Low Voltage	VoL			0.3VDDIO	v	V <sub>DDIO</sub> =3.3V, I <sub>OL</sub> =10mA

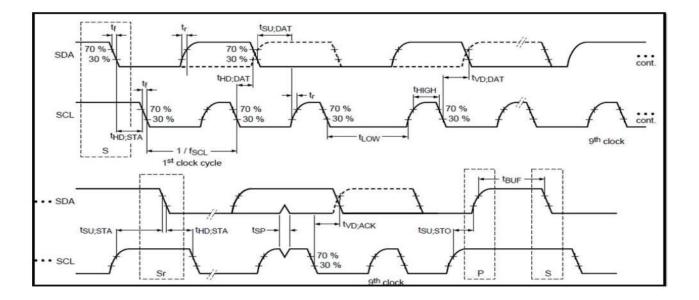
#### Table 7-5:USB DC Characteristics

Item	Symbol	Min	Тур.	Max	Unit	Condition
Input Low	VIL			0.8	V	
Input High (driven)	VIH	2.0			V	
Differential input sensitivity	V <sub>DI</sub>	0.2			V	(D+) - (D-)
Differential common-mode range	V <sub>CM</sub>	0.8		2.5	V	Includes Voi range
Single-ended receiver threshold	VSE	0.8		2.0	V	
Receiver hysteresis	V <sub>RH</sub>		200		mV	
Output low (driven)	VoL	0		0.3	V	
Output high (driven)	V <sub>он</sub>	2.8		3.6	V	
Output signal cross voltage	VCRS	1.3		2.0	V	
Pull-up resistor	R <sub>PU</sub>	1.425		1.575	kΩ	
Pull-down resistor	R <sub>PD</sub>	14.25		15.75	kΩ	
Termination Voltage for upstream port pull up (RPU)	V <sub>TRM</sub>	3.0		3.6	v	

#### Table 7-6:Crystal Characteristics

Item	Symbol	Min	Тур.	Max	Unit	Condition
Input clock frequency	f <sub>XIN</sub>		12		MHz	External crystal

## 7.4 I2C AC Characteristics



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#### Table 7-7:I2C DC Characteristics

	80.00	Standard	-mode	Fast-mod	1	
Parameter	Symbol	Min	Max	Min	e Max 400 - - - 300 300 - - 400 -	Unit
SCL clock frequency	f <sub>SCL</sub>	0	100	0	400	kHz
Hold time START condition	t <sub>HD;STA</sub>	4.0	-	0.6		us
LOW period of the SCL clock	tLow	4.7	2	1.3	1	US
HIGH period of the SCL clock	t <sub>High</sub>	4.0	<b>1</b>	0.6	142	us
Set-up time for a repeated START condition	t <sub>su:sta</sub>	4.7		0.6		us
Data hold time	thd;dat	300	<b>1</b>	300	142	ns
Data set-up time	t <sub>su;dat</sub>	250	12 E	100	-	ns
Rise time of both SDA and SCL signals (30% to 70%)	te		1000	20	300	ns
Fall time of both SDA and SCL signals (70% to 30%)	ţ	-	300	20	300	ns
Set-up time for STOP condition	tsu;sto	4.0	14 A	0.6	1483	us
Bus free time between a STOP and START condition	t <sub>eu≓</sub>	4.7		1.3		US
Capacitive load for each bus line	Cb	2	400		400	pF
Noise margin at the LOW level for each connected device	VnL	0.1V <sub>DD</sub>		0.1V <sub>DD</sub>		v
Noise margin at the HIGH level for each connected device	VnH	0.2Vpp	-	0.2Vpp	04	v

\*SCL = I2C Host must to support clock stretching mode for using 400 kHz.



## 8.Typical Operation Conditions Test condition: GND=0V, TA=25 °C

ltem	Symphol	,	Values	11:0:4	Remark	
nem	Symbol	Min.	Тур.	Max.	Unit	Reindik
Power voltage	LCD_VCC	3.0	3.3	3.6	V	
Current for Driver	LCD_VCC	-	280	-	MA	
Voltage for LED Backlight	VL	24	28	33	V	Note 1
Crrent for LED Backlight	١L	-	220	-	mA	
LED life time	-	50,000	-	-	Hr	Note 2

Note1: V<sub>L</sub>=28V, I<sub>L</sub>=220mA (Backlight circuit: 10series connection, 6 parallel connection), the ambient temperature is 25℃.

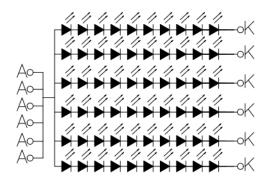


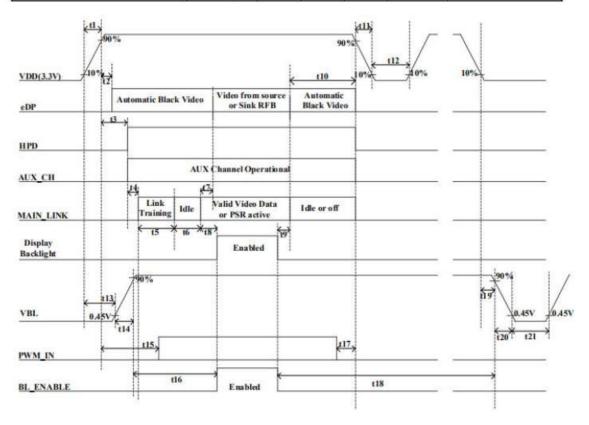
Fig. 3-1 LED test circuit diagram

Note 2: The "LED life time" is defined as the module brightness decrease to 50% original brightness at Ta=25°C and I<sub>L</sub> =220mA . The LED lifetime could be decreased if operating I<sub>L</sub> is larger than 320 mA.



# 8.1 Power Sequence

						Ta=+25%
	DC Elec	trical Cha	aracteristic	cs		
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
+3.3V supply voltage	VDD	+3.0	+3.3	+3.6	V	[Note 5-2-1]
Current dissipation	IDD	-	-		mA	[Note 5-2-2]
Permissible input ripple voltage	VRP	340	- Si	100	mVp-p	VDD=+3.3V
	eDP AUX (	Channel G	Characteri	stics		h
Parameter	Symbol	Min.	Тур.	Max.	Unit	Remark
Unit Interval for AUX channel	UIAUX	0.4	0.5	0.6	μs	
Peak-to-peak voltage at TP1	VAUX-DIFF-pp	0.32	-	1.36	v	
AUX DC Common mode Voltage	VAUX-DC-CM	0		2.0	v	
AUX Short current limit	IAUX_SHORT		-	90	mA	
AUX CH terminationDCresistor	RAUX TERM		100		Ω	Differential input
AUX AC coupling capacitor	CAUX	75		200	nF	
Number of pre-charge pulses	Pre-charge pulses	10	*	16		
	eDP Main Lin	k Receive	er Charact	teristics		
Parameter	Symbol	Min.	Typ.	Max.	Unit	Remark
Link clock down spreading	Down_Spread_Am plitude	0		0.5	%	
Differential Peak-to-peak Input Voltage at Rx package pins	V <sub>RX-DBFp-p</sub>	90	-	1200	mV	
Differential Return Loss at 1.35GHz at Rx package pins	RL <sub>RX-DIFF</sub>	9	84	2	dB	
Differential termination resistance	R <sub>RX-TERM</sub>		100	- 2	Ω	
RX short circuit Current Limit	IRX-SHORT			50	mA	
Lane Intra-pair Skew at RX package pins	T <sub>RX-SKEW-INTRA-P</sub> AIR-High-Bit- Rate			50	ps	



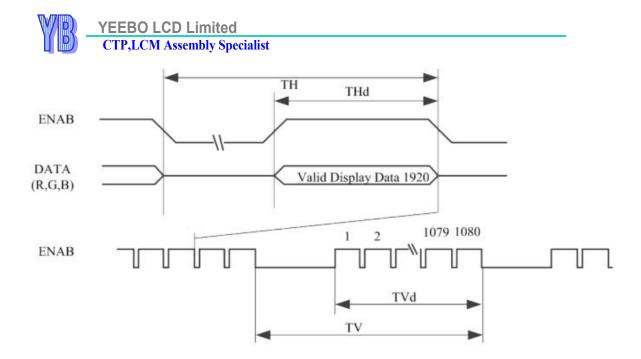
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Symbol	Min	Max	Unit	Note
t1	0.5	10	ms	
t2	0	200	ms	
t3	0	100	ms	
t4	-	-	ms	
t5	-		ms	
t6	-	-	ms	
t7	0	50	ms	
t8	-	-	ms	
t9	-	54	ms	
t10	0	500	ms	
t11	1	50	ms	[Note 5-2-3]
t12	500	-	ms	
t13	-	-	ms	
t14	0.5	10	ms	
t15	100	-	ms	
t16	0	-	ms	
t17	0	-	ms	
t18	-	-	ms	
t19	-	-	ms	
t20	0.1	-	ms	
t21	100	-	ms	

# 8.2Signal Timing Characteristics

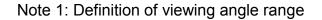
Parar	Symbol	Min.	Тур.	Max.	Unit	Remark	
Clock	Frequency	1/T <sub>C</sub>	132.0	138.5	140.0	MHz	[Note 6-1-1]
	Hariage to be available	TH	2020	2080	2400	clock	
	Horizontal period	TH		15.02		μs	
D	Horizontal period (High)	THd	2 <del></del>	1920	-	clock	
Data enable signal	Vertical period	TV	1090	1111	1200	line	
			-	16.685		ms	-
	Vertical period (High)	TVd	-	1080	-	line	

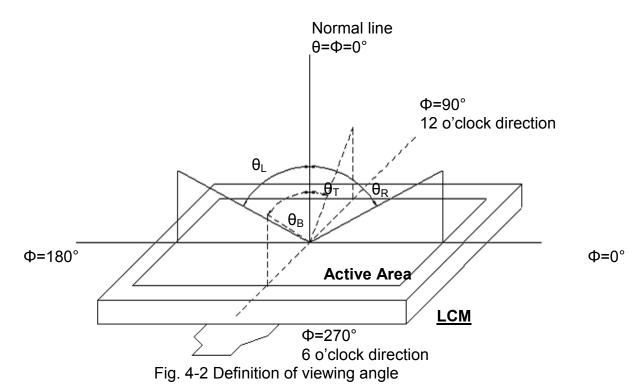


# 9. Optical Specifications

li e ree	Or make at	O a maliti a m		Values		11	Demente
ltem	Symbol	Condition	Min.	Тур.	Max.	Unit	Remark
	θL	Φ=180° (9 o'clock)	-	89	-		
Viewing angle	θR	Φ=0°(3 o'clock)	-	89	-		
(CR≥ 10)	θΤ	Φ=90° (12 o'clock)	-	89	-	degree	Note 1
	θВ	Φ=270° (6 o'clock)	-	89	-		
Response time	TON+ TOFF	, , , , , , , , , , , , , , , , , , ,	-	25	35	msec	Note 2
Contrast ratio	CR		700	1000	-	-	Note 3
Color	WX	Normal	0.27	0.31	0.35	-	Note 4
chromaticity	WY	$\theta = \Phi = 0^{\circ}$	0.27	0.31	0.35	-	Note 5 Note 6
Luminance	L	-	500	590	-	cd/m <sup>2</sup>	Note 6
Luminance uniformity	YU		70	75	-	%	Note 7

The test systems refer to Note 2.





Note 2: Definition of Response time

The response time is defined as the LCD optical switching time interval between "White" state and "Black" state. Rise time ( $T_{ON}$ ) is the time between photo detector output intensity changed from 90% to 10%. And fall time ( $T_{OFF}$ ) is the time between photo detector output intensity changed from 10% to 90%.

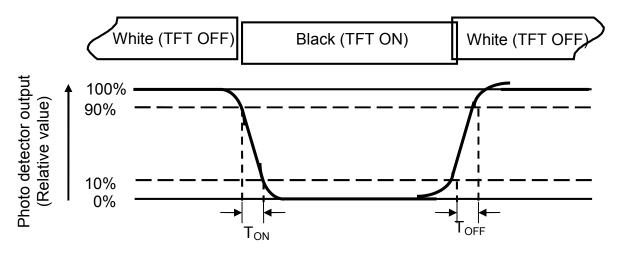
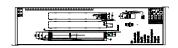


Fig. 4-3 Definition of response time

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Note 3: Definition of contrast ratio



Note 4: Definition of optical measurement system.

The optical characteristics should be measured in dark room. After 30 minutes operation, the optical properties are measured at the center point of the LCD screen. (Viewing angle is measured by ELDIM-EZ contrast/Height :1.2mm, Response time is measured by Photo detector TOPCON BM-7, other items are measured by BM-5A/ Field of view: 1° /Height: 500mm.) or CA-210.

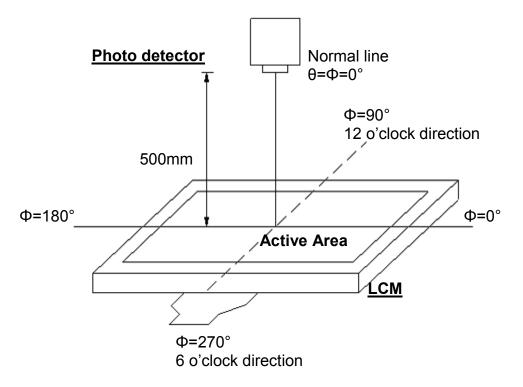
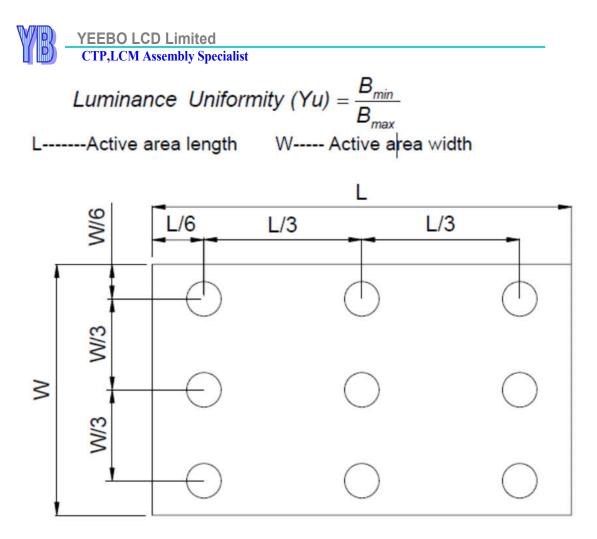


Fig. 4-4 Optical measurement system setup

- Note 5: Definition of color chromaticity (CIE1931) Color coordinates measured at center point of LCD.
- Note 6: All input terminals LCD panel must be ground while measuring the center area of the panel. The LED driving condition is  $I_L$ =200mA .

Note 7: Definition of Luminance Uniformity Active area is divided into 9 measuring areas(Refer to Fig. 4-5). Every measuring point is placed at the center of each measuring area.



 $B_{\text{MAX}}$ : The measured maximum luminance of all measurement position.  $B_{\text{MIN}}$ : The measured minimum luminance of all measurement position.



**10.Reliability Test Items** 10–1. Standard Specifications for Reliability of LCD Module

No	Item	Description
01	High temperature operation	The sample should be allowed to stand at $60^{\circ}$ C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
02	Low temperature operation	The sample should be allowed to stand at $-10^{\circ}$ C for 120 hours under driving condition and then returning it to normal temperature condition, and allowing it stand for 2 hours.
03	High temperature storage	The sample should be allowed to stand at $70^{\circ}$ C for 240 hours under no-load condition, and then returning it to normal temperature condition and allowing it stand for 2 hours.
04	Low temperature storage	The sample should be allowed to stand at $-30^{\circ}$ C for 240 hours under no-load condition, then returning it to normal temperature condition, and allowing it stand for 2 hours.
05	Moisture storage	The sample should be allowed to stand at 50°C, 90%RH MAX for 240 hours under no-load condition, then taking it out and drying it at normal temperature for 2 hours.
06	Thermal shock storage	The sample should be allowed to stand the following 10 cycles : $0^{\circ}$ C for 30 minutes $\rightarrow$ normal temperature for 5 minutes $\rightarrow$ +60°C for 30 minutes $\rightarrow$ normal temperature for 5 minutes, as one cycle.
07	Packing vibration	Frequency range : 10Hz ~ 55Hz Amplitude of vibration : 1.5mm Sweep time: 12 min X, Y, Z 2 hours for each direction.

\*Sample size for each test item is 3~5pcs



#### 10 - 2. Testing Conditions and Inspection Criteria

For the final test the testing sample must be stored at room temperature for 24 hours, after the tests listed in Table 10-1, Standard specifications for Reliability have been executed in order to ensure stability.

No	Item	Test Model	In section Criteria
01	Current Consumption	Refer To Specification	The current consumption should conform to the product specification.
02	Contrast	Refer To Specification	After the tests have been executed, the contrast must be larger than half of its initial value prior to the tests.
03	Appearance	Visual inspection	Defect free.

#### 10-3. MTBF

MTBF	Functions, performance, appearance, etc. shall be free from remarkable deterioration within 50,000 hours under ordinary operating and storage conditions room temperature ( $25\pm5$ °C), normal humidity ( $50\pm10\%$ RH), and in area not exposed to direct sun light.
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## **<u>11. Specification of Quality Assurance:</u>**

11-1. Purpose

This standard for Quality Assurance should affirm the quality of LCD module products to supply to purchaser by YEEBO CORPORATION (Supplier).

11-2. Standard for Quality Test

a. Inspection:

Before delivering, the supplier should take the following tests, and affirm the quality of

#### product.

b. Electro-Optical Characteristics:

According to the individual specification to test the product.

- c. Test of Appearance Characteristics:
  - According to the individual specification to test the product.
- d. Test of Reliability Characteristics:

According to the definition of reliability on the specification for testing products.

e. Delivery Test:

Before delivering, the supplier should take the delivery test.

(i) Test method: According to ISO2859-1. General Inspection Level II take a single time.

(ii) The defects classify of AQL as following:

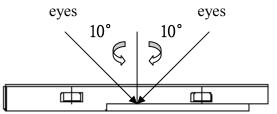
Major defect: AQL = 0.65%

- Minor defect: AQL = 2.5%
- Total defects: AQL = 2.5%
- 11-3. Non- conforming Analysis & Deal With Manners
  - a. Non- conforming Analysis:
    - (i) Purchaser should supply the detail data of non- conforming sample and the non-conforming.
    - (ii) After accepting the detail data from purchaser, the analysis of non- conforming should be finished in two weeks.
    - (iii) If supplier can not finish analysis on time, must announce purchaser before 3 days.
  - b. Disposition of non- conforming:
    - (i) If find any product defect of supplier during assembly time, supplier must change the good product for every defect after recognition.
    - (ii) Both supplier and customer should analyze the reason and discuss the disposition of non- conforming when the reason of nonconforming is not sure.
- 11-4. Agreement items
  - Both sides should discuss together when the following problems happen.
  - a. There is any problem of standard of quality assurance, and both sides should think that must be modified.
  - b. There is any argument item which does not record in the standard of quality assurance.
  - c. Any other special problem.

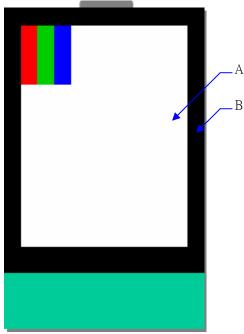
- 11-5. Standard of The Product Appearance Test
  - a. Manner of appearance test:

(i) The test must be under 20W  $\times$  2 or 40W fluorescent light, and the distance of view must be at 30±5cm.

- (ii) When test the model of transmissive product must add the reflective plate.
- (iii)The test direction is base on around  $10^{\circ}$  of vertical line.
- (iiii)Temperature: 25±5°C Humidity: 60±10%RH



(iv) Definition of area:



- A. Area: Viewing area.
- B. Area: Out of viewing area.

(Outside viewing area)

- b. Basic principle:
- (i) It will accord to the AQL when the standard can not be described.
- (ii) The sample of the lowest acceptable quality level must be discussed by both supplier and customer when any dispute happened.
- (iii) Must add new item on time when it is necessary.
- c. Standard of inspection: (Unit: mm)



## 11-6. Inspection specification

ltem		Specificatio	Unit : mm	AQL				
Electrical Testing	<ol> <li>1.4 Missing</li> <li>1.5 Missing</li> <li>1.6 Display</li> <li>1.7 No func</li> <li>1.8 Current</li> <li>1.9 LCD vie</li> </ol>	<ul> <li>2 Short</li> <li>3 T/P failure</li> <li>4 Missing vertical, horizontal segment, segment contrast defect.</li> <li>5 Missing character, dot or icon.</li> <li>6 Display malfunction.</li> <li>7 No function or no display.</li> <li>8 Current consumption exceeds product specifications.</li> <li>9 LCD viewing angle defect.</li> <li>10 Mixed product types.</li> </ul>						
explosion-proof film bubble/Concave and convex point/indentation / Contamination	Product typeDAcceptable numbers $I = 1 + 1 + 1 + 1 + 1 + 1 + 1 + 1 + 1 + 1$							
	<ul><li>2、 Printin</li><li>3、 The pa</li></ul>	-						



	Product type	D		ceptable numbers				
			ignor	ignored (No more				
		≪0.2	that	n five spots				
			wi	thin 5mm)	$\frac{X}{D=(x+y)/2}$			
	LAD	0.2≤D≤0.4		3	D = (x+y)/2			
Black spots /		0.4 <d≤0.8< td=""><td></td><td>2</td><td></td><td></td></d≤0.8<>		2				
White spots		D>0.8		NG				
/Bright spots/ Color spots /polluted inside/ punctured		1.Product's front side checked according to this specification, back side gnored, but light leakage is not allowed.						
	2.Printing in	nk peel off is not al	llowed.					
	3、The part	ticle will be ignore	d when	it is removable b	by cleaning			
	* Densely s	paced: No more th	an two s	pots within 10m	ım			
	Product	<b>XX</b> 7	L	A				
	type	W		Acceptable number				
			≪8	ignored Nor				
		≪0.05		than five lin				
	LAD			within 5mm	1)			
Linger Ohiget.		0.1<₩≤0.3	≪8	2				
-		₩> 0.3		NG				
Linear Object: Fiber, scurf, scratches and other linear defects (not affecting function)	scratches from $W$	side scratches, not om the front side is paced: No more th	s accepta	able	circuit, cannot find the	2.5		



	s edge bing、edge cage		e can' t affect visual effection (edge 't cause damage to circuit); over no visual damage conditions Acceptabl e numbers $X \leq 3.0 \text{nm}, Y \leq 2.0 \text{nm}, Z \leq T$ 5	2.5					
Glass	broken	Visual broken is NG, and there is no potential fault.							
edges insj accord this 2. LO	1. V/A printed edges sawtooth inspected according to this standard       Some contentious defect judged according to samples         Product this standard       Product type       Conditions         2. LOGO's sawtooth       1、 width below 0.2 inch (included) ignored, above 0.2 NG 2、 Length not accounted       Image: Condition of the samples				2.5				
Specif	Specific dimension In accordance with product outline drawing or specification (key dimension) or engineering sample.								
Glue overfl	ow/Frame	1. (	Glue overflow	exceed 0.2mm to the black frame is not allowed.	2.5				
	Bonding bubble/ Misalignm ent	1/2	FPC golden finger hot pressure's bubble or impurity diameter shall be below 1/2 of the pressed area, pressed deviation shall not exceed 1/2 of the silver line 0. width, and 40X microscope cannot have obvious cracks.						
FPC	Folded mark (minor fault)	Lin	Linearity irreversibility folded mark and acute angle folded mark is NG.						
		Surface broken, scratched ≤0.3mm Surface broken below 5mm can be modified by print ink, after modified, the result shall be achieved to EMI							



## **12. Handling Precaution:**

#### 12.1 Warranty

This product has been manufactured to specifications as a part for use in your company's general electronic products. It is guaranteed to perform according to delivery specifications. For any other use apart from general electronic equipment, we will not take responsibility if the product is used in medical devices, nuclear power control equipment, aerospace equipment, fire and security systems, or any other applications in which there is a direct risk to human life and where extremely high levels of reliability are required. If the product is to be used in any of the above applications, we will need to enter into a separate product liability agreement.

- 1. We cannot accept responsibility for any defect arise after additional process of the product (including disassembly and reassembly), after product delivery.
- 2. We cannot accept responsibility for any defect, which may arise after the application of strong external force to the product.
- 3. We cannot accept responsibility for any defect, which may arise due to the application of static electricity after the product has passed your company's acceptance inspection procedures.
- 4. We cannot accept responsibility for industrial property, which may arise through the use of your product, with exception to those issues relating directly to the structure or method of manufacturing of our product 3months from YEEBO production.
- 5. The liability of YB is limited to repair or replacement on the terms set forth below. YB will not be responsible for any subsequent or consequential events or injury or damage to any personnel or user including third party personnel and/or user. Unless otherwise agreed in writing between YB and the customer, YB will only replace or repair any of its CTP which is found defective electrically or visually when inspected in accordance with YB GENERAL CTP INSPECTION STANDARD.

## 12.2. Precautions in Use of CTP Module

#### 12.2-1. Handling of CTP Module

12.2-1-1 Please operate the capacitive touch panel by touch the panel surface with finger or electric pen

12.2-1-2 Store the products at the temperature and humidity mentioned in the specification in a good package do not expose the products under direct sunlight.

12.2-1-3 Do not hit the capacitive touch panel in strong force, or drop it down, it is made of glass and friable.

12.2-1-4 Put on finger coats, glovers or mask to protect the products from fingerprint of stain. Do not upload/unload the touch panel by holding the FPC cable. Do not bend the FPC cableoften or pull it hard when installing, as FPC cable is soft and connected to touch panel body.

12.2-1-5 Pay attention to the prevention from high voltage and static electricity.

## 12.2-2 Storage

12.2-2-1 Store in ambient temperature of 25±5°C, and relative humidity of 50±10%RH. Do not expose to sunlight or fluorescent light.

12.2-2-2 Storage in a clean environment, free from dust, active gas, and solvent.

12.2-2-3 Store in anti-static electricity container.

12.2-2-4 Store without any physical load.

12.2-2-5 Appearance, 3months; Function, 1 year; within the validity, failed CTP can be replaced 1 to 1

## 12.3 Guarantee

Our products meet requirements of the environment.YEEBO ROHS requirement is based on European Union Directive 2011/65/EU (ROHS) Requirements and Update.

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